



SURFACE MOUNT MODEL VOLTAGE CONTROLLED OSCILLATOR

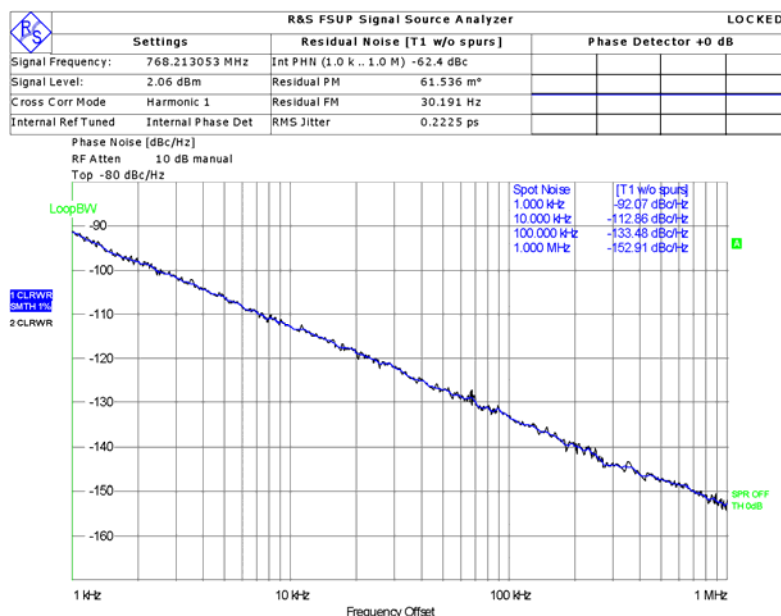
Frequency 758-778MHz

VT778M



FEATURES

- ▶ Miniature Size
- ▶ Surface Mount Package
- ▶ Low Phase Noise Performance
- ▶ Electrically Shielded
- ▶ Highly Linear Tuning



Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{CC} = +5.0\text{V}$ (unless otherwise stated)

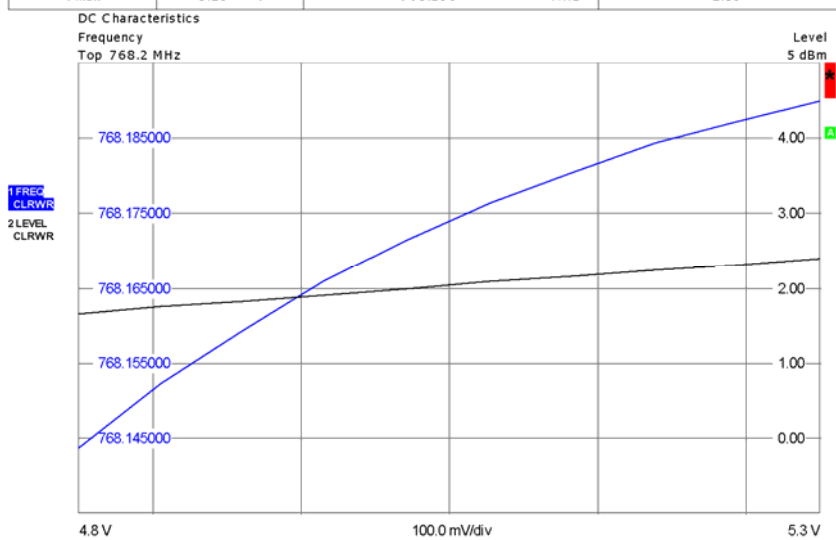
Parameter	Test Conditions	Min	Typ	Max	Units
Frequency Rangs		758		778	MHz
Tuning Voltage (V)		0.5		4.5	V
RF Output Power	758-778MHz	0.0		4.0	dBm
Supply Voltage (Vcc)		4.75		5.25	V
Supply Current (Icc)			13	16	mA
Phase Noise :					
	@10kHz Offset:		-112	-109	dBc/Hz
	@100kHz Offset:		-133	-130	dBc/Hz
Average Tuning Sensitivity	758-778MHz		10.2		MHz/V
Harmonic Outputs			-20		dBc
OperatingTemperature		-40		+85	°C
Tune Input Capacitance			47		PF
Output Impedance -			50		Ω
Package Type	SK605(12.7X12.7X4.3mm)				

Comments

X Indicates parameter to be tested 100% in production

V TUNE	TUNING SENS. (MHz/V)	FREQUENCY(MHz)			POWER OUTPUT(dBm)			HARMONIC(dBc)		
		-40°C	+25°C	+85°C	-40°C	+25°C	+85°C	-40°C	+25°C	+85°C
0.5	9.5	752.1	747.2	743.8	2.0	1.3	0.4	-26.3	-21.2	-20.7
1.0	9.1	757.0	752.2	748.9	2.1	1.3	0.4	-26.8	-21.6	-21.3
1.5	9.1	761.9	757.0	753.8	2.2	1.4	0.5	-27.2	-22.0	-21.9
2.0	9.5	766.9	761.9	758.6	2.2	1.5	0.5	-27.5	-22.3	-22.3
2.5	9.7	772.1	767.0	763.6	2.3	1.5	0.5	-27.8	-22.6	-23.2
3.0	10.1	777.5	772.2	768.9	2.3	1.5	0.4	-28.0	-23.0	-23.9
3.5	10.5	783.0	777.8	774.4	2.3	1.4	0.2	-28.6	-23.4	-24.6
4.0	10.5	788.5	783.3	779.8	2.4	1.5	0.1	-29.5	-24.1	-25.1
4.5	10.5	793.9	788.7	785.3	2.4	1.5	-0.1	-30.2	-25.1	-26.5

R&S FSUP Signal Source Analyzer					
Settings			Results		
Supply	Voltage		Frequency		Level
Vmin	4.75	V	768.144	MHz	1.65 dBm
Vcurrent	5.00	V	768.174	MHz	2.03 dBm
Vmax	5.25	V	768.190	MHz	2.39 dBm



Measurement Aborted

Date: 27.JUN.2012 11:50:50

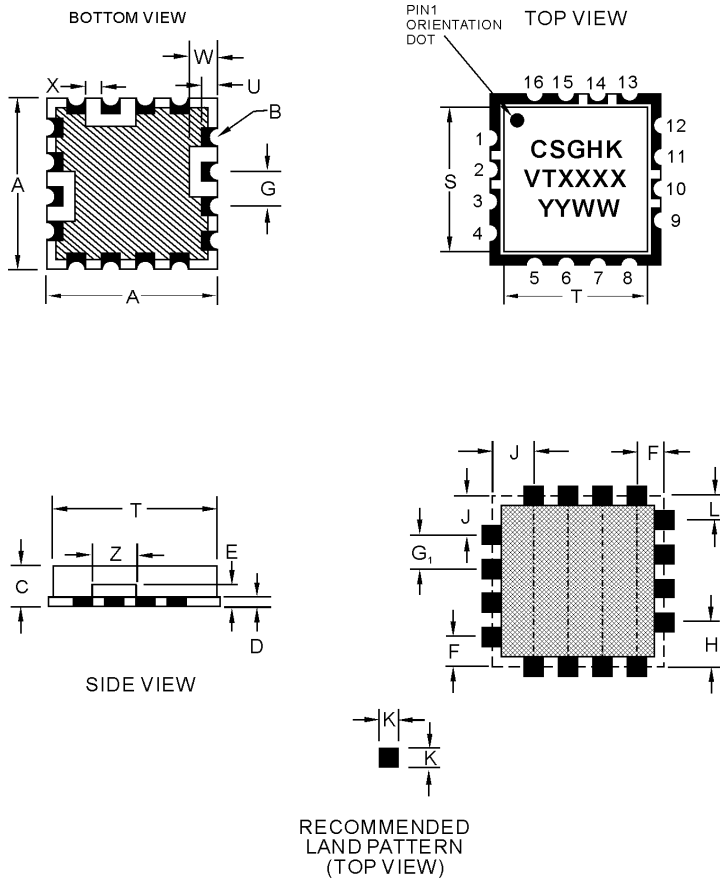
REVISIONS

NOTE. UNLESS OTHERWISE SPECIFIED:

1. THE METAL CASE IS GROUND
2. ALL HALF VIA CONTACTS ARE PLATED THRU FROM THE PAD ON THE TOP SIDE TO THE PAD ON THE BOTTOM SIDE OF THE BOARD.
3. HATCHED AREAS GROUND AND ARE COVERED WITH LPI SOLDER MASK OVER BARE COPPER. ALL CONTACT AREAS ARE PLATED. SIGNAL VIAS MAY BE LOCATED WITHIN GROUND PLANE.
4. SUBSTRATE MATERIAL: FR-4
5. XXXX REPRESENTS THE MODEL NUMBER.
6. YYWW IS THE DATA CODE.

PIN OUT FOR PLL	
PIN	APPLICATION
2	VT
10	RF OUT
14	VCC

ALL OTHER PINS ARE GROUND



SYMBOL	DIMENSION	
	MILLIMETERS	INCHES
A	12.7±0.25	0.50±0.010
B	R=0.50	R=0.019
C	4.3	0.169
D	0.99MAX	0.039MAX
E	1.75MAX	0.069MAX
F	2.16	0.085
G	2.54±0.08	0.100±0.003
G ₁	2.54	0.100
H	3.07	0.121
J	2.92	0.115
K	1.52	0.060
L	2.00	0.079
S	11.68	0.460
T	11.81	0.465
U	1.10	0.043
W	1.78	0.188
X	1.02	0.040
Z	3.49	0.137

	APPROVALS	DATA	 SK605 PACKAGE MECHANICAL PCB OUTLINE
DRAWN BY			
CHECKED BY			
PROCESS ENG			
QUALITY			